

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Analog. 6830CON

APPLICANT: Gormley et al.

GROUP: 2813

SERIAL NO: 10/786,426

EXAMINER: Tuan H. Nguyen

FILED: 02/25/2004

FOR: SEMICONDUCTOR WAFER COMPRISING MICRO-MACHINED
COMPONENTS AND A METHOD FOR FABRICATING THE
SEMICONDUCTOR WAFER

Mail Stop: Issue Fee
Assistant Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

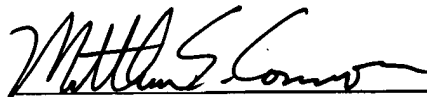
Sir:

CHANGE OF CORRESPONDENCE ADDRESS

Applicant's representative requests that the correspondence address for the above application be changed to:

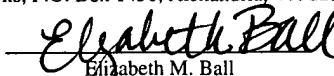
Matthew E. Connors
Gauthier & Connors LLP
225 Franklin Street, Suite 2300
Boston, MA 02110

Respectfully submitted,



Matthew E. Connors
Registration No. 33,298
Gauthier & Connors LLP
225 Franklin Street, Suite 2300
Boston, Massachusetts 02110
Telephone: (617) 426-9180
Extension 112

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450, Mail Stop Issue fee.



Elizabeth M. Ball

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